

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	4090722
<b>Application Number:</b>	10758802
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	6500
<b>Title of Invention:</b>	Methods of fabricating a semiconductor device having multi-gate insulation layers and semiconductor devices fabricated thereby
<b>First Named Inventor/Applicant Name:</b>	Dae-Woong Kang
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<b>Application Type:</b>	Utility under 35 USC 111(a)

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### File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Information Disclosure Statement (IDS) Filed (SB/08)	US_IDS_Form_SB_08a.pdf	574011 2c10b4e6ca2b34a99b0e42512149d11b0e0b4f	no	4
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2		Cited_References.pdf	2221942 015c1581bd4975d8ff0024da7d08a653dd6c8a99	yes	26
<b>Multipart Description/PDF files in .zip description</b>					
		<b>Document Description</b>	<b>Start</b>	<b>End</b>	
		Foreign Reference	1	14	
		Foreign Reference	15	26	
<b>Warnings:</b>					
<b>Information:</b>					
3	Fee Worksheet (PTO-06)	fee-info.pdf	29990 4d38601945f086d122a0736d70f65bc920ff2301	no	2
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<b>Total Files Size (in bytes):</b>			2825943		
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